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2. **Fabrication of photonic crystal membranes in chalcogenide glasses by focused ion beam milling**
Freeman, D.; Madden, S.; Luther-Davies, B.; Grillet, C.; Asatryan, A.A.; Dossou, K.; Byrne, M.A.; Botten, L.C.;
Lasers and Electro-Optics, 2005. (CLEO). Conference on
Volume 1, 22-27 May 2005 Page(s):71 - 73 Vol. 1
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3. **Modal formulation for plane wave scattering by a photonic crystal slab: Fano resonances**
Botten, L.C.; Byrne, M.A.; Asatryan, A.A.; Nicorovici, N.A.; Norton, A.H.; McPhedran, R.C.;
Lasers and Electro-Optics Society, 2005. LEOS 2005. The 18th Annual Meeting of the IEEE
23-27 Oct. 2005 Page(s):390 - 391
Digital Object Identifier 10.1109/LEOS.2005.1548042
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Byrne, M.; Fedor, L.; Hammond, D.; Lorell, J.; Mongnard, N.;
OCEANS
Volume 11, Sep 1979 Page(s):659 - 659
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5. **Advanced techniques for automatic surface search radar correlation and tracking**
Yu, M.; Meyer, M.; Byrne, M.; Winston, H.;
Radar Conference, 1995., Record of the IEEE 1995 International
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Digital Object Identifier 10.1109/RADAR.1995.522562
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Decker, F.-J.; Akre, R.; Byrne, M.; Farkas, Z.D.; Jarvis, H.; Jobe, K.; Koontz, R.; Mitchell, M.;

Pennacchi, R.; Ross, M.; Smith, H.;
Particle Accelerator Conference, 1995., Proceedings of the 1995
Volume 3, 1-5 May 1995 Page(s):1821 - 1823 vol.3
Digital Object Identifier 10.1109/PAC.1995.505373

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7. **A deconvolution .method to quantify rapid pulses in insulin secretion**
Sparacino, G.; Sturio, J.; O'Meara, N.; Byrne, M.; De Nicolao, G.; Polonskya, K.; Cobelli, C.;
Engineering in Medicine and Biology Society, 1993. Proceedings of the 15th Annual International Conference of the IEEE
Oct 28-31, 1993 Page(s):536 - 537

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23-27 Oct. 2005 Page(s):390 - 391
Digital Object Identifier 10.1109/LEOS.2005.1548042
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Digital Object Identifier 10.1109/RADAR.1995.522562
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Engineering in Medicine and Biology Society, 1993. Proceedings of the 15th Annual International Conference of the IEEE
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1 Unintended effects: varying icon spacing changes users' visual search strategy
Sarah P. Everett, Michael D. Byrne
April 2004 **Proceedings of the SIGCHI conference on Human factors in computing systems**
Publisher: ACM Press
Full text available: pdf(675.36 KB) Additional Information: full citation, abstract, references, index terms

Users of modern GUIs routinely engage in visual searches for various control items, such as buttons and icons. Because this is so ubiquitous, it is important that the visual properties of user interfaces support such searches. The current research is aimed at deepening our understanding of how the visual spacing between icons affects visual search times. We constructed an experiment based on previous icon sets [8] where spacing between icons was systematically manipulated, and for which we had a ...

Keywords: iconic displays, user and cognitive models, visual search

2 Effects of scent and breadth on use of site-specific search on e-commerce Web sites
Michael A. Katz, Michael D. Byrne
September 2003 **ACM Transactions on Computer-Human Interaction (TOCHI)**, Volume 10 Issue 3
Publisher: ACM Press
Full text available: pdf(2.34 MB) Additional Information: full citation, abstract, references, index terms

Users faced with Web sites containing many possibly relevant pages often have a decision to make about navigation: use the menu of links or use the provided site search function. Two studies were conducted to examine what users do when faced with this decision on e-commerce Web sites, and how users go about deciding which method to attempt. An exploratory study revealed a wide distribution of searching and browsing behavior across sites and users. Counter to some predictions, use of the site sea ...

Keywords: Searching, WWW, browsing, e-commerce, empirical studies, information retrieval, navigation

3 Eye tracking the visual search of click-down menus
Michael D. Byrne, John R. Anderson, Scott Douglass, Michael Matessa
May 1999 **Proceedings of the SIGCHI conference on Human factors in computing systems: the CHI is the limit**
Publisher: ACM Press
Full text available: pdf(830.18 KB) Additional Information: full citation, abstract, references, citings, index terms

Click-down (or pull-down) menus have long been a key component of graphical user interfaces, yet we know surprisingly little about how users actually interact with such menus. Nilsens [8] study on menu selection has led to the development of a number of models of how users perform the task [6, 21. However, the validity of these models has not been empirically assessed with respect to eye movements (though [I] presents some interesting data that bear on these models). The present study ...

Keywords: cognitive models, eye tracking, menu selection, visual search

4 Inside risks: Disability-related risks

Peter G. Neumann, Michael D. Byrne

August 2005 **Communications of the ACM**, Volume 48 Issue 8

Publisher: ACM Press

Full text available: pdf(48.65 KB) html(7.91 KB) Additional Information: full citation, index terms

5 A comparison of tools for building GOMS models

Lynn K. Baumeister, Bonnie E. John, Michael D. Byrne

April 2000 **Proceedings of the SIGCHI conference on Human factors in computing systems**

Publisher: ACM Press

Full text available: pdf(1.02 MB) Additional Information: full citation, abstract, references, citings, index terms

We compare three tools for creating GOMS models, QGOMS [2), CATHCI (17) and GLEAN3 [12], along several dimensions. We examine the representation and available constructs in each tool, the qualitative and quantitative design information provided, the support for building cognitively plausible models, and pragmatics about using each tool (e.g., how easy it is to modify a model). While each tool has its strengths, they all leave something to be desired as a practical UI design tool.

Keywords: GOMS, tool support for evaluation

6 A tool for creating predictive performance models from user interface demonstrations

Scott E. Hudson, Bonnie E. John, Keith Knudsen, Michael D. Byrne

November 1999 **Proceedings of the 12th annual ACM symposium on User interface software and technology**

Publisher: ACM Press

Full text available: pdf(113.85 KB) Additional Information: full citation, abstract, references, citings, index terms

A central goal of many user interface development tools has been to make the construction of high quality interfaces easy enough that iterative design approaches could be a practical reality. In the last 15 years significant advances in this regard have been achieved. However, the evaluation portion of the iterative design process has received relatively little support from tools. Even though advances have also been made in usability evaluation methods, nearly all evaluation is still done & ...

Keywords: GOMS, event logs, task modeling, tool support for evaluation, toolkits

7 The tangled Web we wove: a taskonomy of WWW use

Michael D. Byrne, Bonnie E. John, Neil S. Wehrle, David C. Crow

May 1999 **Proceedings of the SIGCHI conference on Human factors in computing**

systems: the CHI is the limit
Publisher: ACM Press

Full text available: pdf(1.08 MB) Additional Information: full citation, abstract, references, citings, index terms

A prerequisite to the effective design of user interfaces is an understanding of the tasks for which that interface will actually be used. Surprisingly little task analysis has appeared for one of the most discussed and fastest-growing computer applications, browsing the World-Wide Web (WWW). Based on naturally-collected verbal protocol data, we present a taxonomy of tasks undertaken on the WWW. The data reveal that several previous claims about browsing behavior are questionable, and ...

Keywords: World-Wide Web, task analysis, video protocols

8 Automating interface evaluation

Michael D. Byrne, D. Wood, Piyawadee Sukaviriya, James D. Foley, David Kieras
April 1994 **Conference companion on Human factors in computing systems**
Publisher: ACM Press
Full text available: pdf(77.47 KB) Additional Information: full citation

9 A computational theory of working memory

Michael D. Byrne
April 1996 **Conference companion on Human factors in computing systems: common ground**
Publisher: ACM Press
Full text available: pdf(221.37 KB) Additional Information: full citation, references, index terms

10 Automating interface evaluation

Michael D. Byrne, Scott D. Wood, James D. Foley, David E. Kieras, Piyawadee Noi Sukaviriya
April 1994 **Proceedings of the SIGCHI conference on Human factors in computing systems: celebrating interdependence**
Publisher: ACM Press
Full text available: pdf(545.80 KB) Additional Information: full citation, references, citings, index terms

Keywords: GOMS, UIMS, formal models of the users, interface evaluation, usability, user interface design environment

11 Using icons to find documents: simplicity is critical

Michael D. Byrne
May 1993 **Proceedings of the SIGCHI conference on Human factors in computing systems**
Publisher: ACM Press
Full text available: pdf(705.11 KB) Additional Information: full citation, abstract, references, index terms

A common task at almost any computer interface is that of searching for documents, which GUIs typically represent with icons. Oddly, little research has been done on the processes underlying icon search. This paper outlines the factors involved in icon search and proposes a model of the process. An experiment was conducted which suggests that the proposed model is sound, and that the most important factor in searching for files is the type of icons used. In general, simple icons (those disc ...

Keywords: empirical evaluation, formal models of the user, icons, screen design

12 Interactive posters: THE MISUNDERSTOOD PICTURE: A STUDY OF ICON RECOGNITION

Michael D. Byrne
October 1991 **ACM SIGCHI Bulletin**, Volume 23 Issue 4
Publisher: ACM Press
Full text available: pdf(221.71 KB) Additional Information: full citation, abstract

Icons are a prevalent feature in current computer systems. Yet, little substantive research has been done on the benefits, drawbacks, ideal properties, and cognitive impact of icons. To some extent, it seems that it has just been assumed that icons are a generally better representation.

13 Late breaking results: posters: Information search: the intersection of visual and semantic space

Franklin P. Tamborello, Michael D. Byrne
April 2005 **CHI '05 extended abstracts on Human factors in computing systems**
Publisher: ACM Press
Full text available: pdf(171.10 KB) Additional Information: full citation, abstract, references, index terms

In the context of an information search task, does the visual salience of items interact with information scent? That is, do things like bold headlines or highlighted phrases interact with local semantic cues about the usefulness of distal sources of information? Most research on visual search and highlighting has used stimuli with no semantic content, while studies on information search have assumed equal visual salience of items in the search space. In real information environments like the We ...

Keywords: content creation, content strategy, information architecture, visual design, world wide web and hypermedia

14 Building and using cultural digital libraries: Supporting access to large digital oral history archives

Samuel Gustman, Dagobert Soergel, Douglas Oard, William Byrne, Michael Picheny, Bhuvana Ramabhadran, Douglas Greenberg
July 2002 **Proceedings of the 2nd ACM/IEEE-CS joint conference on Digital libraries**
Publisher: ACM Press
Full text available: pdf(734.60 KB) Additional Information: full citation, abstract, references, citings, index terms

This paper describes our experience with the creation, indexing, and provision of access to a very large archive of videotaped oral histories - 116,000 hours of digitized interviews in 32 languages from 52,000 survivors, liberators, rescuers, and witnesses of the Nazi Holocaust. It goes on to identify a set of critical research issues that must be addressed if we are to provide full and detailed access to collections of this size: issues in user requirement studies, automatic speech recognition, ...

Keywords: cataloging, oral history, research agenda

15 Ada and the X Window System

Stu Lewin, Kirk Beitz, Christopher Byrnes, Michael Hardy, Rich Hilliard, Craig Warsaw
December 1991 **Proceedings of the conference on TRI-Ada '91: today's accomplishments; tomorrow's expectations**
Publisher: ACM Press
Full text available: pdf(435.56 KB) Additional Information: full citation, index terms

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1 Pseudo-random testing and signature analysis for mixed-signal circuits
Chen-Yang Pan, Kwang-Ting Cheng
December 1995 **Proceedings of the 1995 IEEE/ACM international conference on Computer-aided design**
Publisher: IEEE Computer Society
Full text available: pdf(88.30 KB) Publisher Site
Additional Information: full citation, abstract, references, index terms

In this paper, we address the problem of functional testing of mixed-signal circuits using pseudo-random patterns. By embedding the linear, time-invariant (LTI) analog circuit between a digital-to-analog converter (DAC) and an analog-to-digital converter (ADC), we can model the analog and converter circuitry as a digital LTI system and test it using the pseudo-random vectors. We give mathematical analysis and formulate the pseudo-random testing process as the linear transformation of a random pr ...

Keywords: Pseudo-Random Testing, Random Process, Signature Analysis, Impulse Response

2 A hybrid architecture for neurocomputing (abstract)
W. E. Mattis
January 1990 **Proceedings of the 1990 ACM annual conference on Cooperation**
Publisher: ACM Press
Additional Information: full citation, abstract, index terms

A hybrid (analog/digital) architecture is described for realization as a neurocomputing element. The architecture is derived by examination of the operation of a typical neural network. Specifically, the outputs of neurons are connected to other neurons through axons and dendrites, weighted at the synaptic junction. The next state of any neuron is determined by the sum of the weighted inputs from all other neurons. Neural membranes, modeled as sigmoidal functions, determine the neuron respo ...

3 Student paper competition: Speech compression: a functional approximation approach
Kevan L. Miller
April 1982 **Proceedings of the 20th annual Southeast regional conference ACM-SE 20**
Publisher: ACM Press
Full text available: pdf(79.29 KB) Additional Information: full citation, abstract

This paper presents research undertaken in the field of speech compression with a low

cost speech processing system developed around an APPLE II microcomputer. Unlike some of the more popular techniques of speech compression based on statistical analysis of the speech waveforms in the frequency domain, speech compression was approached by the authors with the perspective of functional approximations of speech waveforms in the time domain. These functional approximations would be evaluated in a p ...

4 Sampling frequency of digital servomechanisms

Julius Tou

January 1956 **Proceedings of the 1956 11th ACM national meeting**

Publisher: ACM Press

Full text available: pdf(151.29 KB) Additional Information: full citation, abstract, index terms

A digital servomechanism is a sampled-data feedback control system utilizing a digital computer to perform sampling, error-detection and digital compensation for the system. It consists essentially of a digital computer, digital-analog converters, and system components. The block diagram of a typical digital servomechanism is shown in Figure 1, in which D(z) is the program or the z-transfer function of a digital compensator in the computer, G(s) stands for the transfer function of the syste ...

5 Digital Computer System for Dynamic Analysis of Speech and Sound Feedback Mechanisms

K. U. Smith, S. D. Ansell, J. Koehler, G. H. Servos

April 1964 **Journal of the ACM (JACM)**, Volume 11 Issue 2

Publisher: ACM Press

Full text available: pdf(1.03 MB) Additional Information: full citation, abstract, references, citings, index terms

The aim of the research reported is to put the laboratory application of closed-loop digital computer systems to experimental control and analysis in biology and behavioral science on a formal basis, using special concepts of programming to quantitatively control different parameters of variation in sensory feedback of specific response systems. The theory is unconventional in that the computer and the techniques of closed-loop programming are designed to control time delays, space displace ...

6 Defect-oriented test methodology for complex mixed-signal circuits

F. C. M. Kuijstermans, M. Sachdev, A. P. Thijssen

March 1995 **Proceedings of the 1995 European conference on Design and Test**

Publisher: IEEE Computer Society

Full text available: pdf(660.89 KB) Publisher Site Additional Information: full citation, abstract, citings

Testing of analog blocks in digital circuits is emerging as a critical factor in the success of mixed-signal ICs. The present specification-oriented testing of these blocks results in high test costs and doesn't ensure detection of all defects, causing potential reliability problems. To solve these problems, in this paper a defect-oriented test methodology for mixed analog-digital circuits is proposed. The strength of the method is demonstrated by an implementation for a complex mixed-signal cir ...

Keywords: CMOS, CMOS integrated circuits, DfT guidelines, analogue-digital conversion, complex mixed-signal circuits, defect coverage, defect-oriented test methodology, design for testability, flash analog-to-digital converter, integrated circuit testing, mixed analog-digital circuits, mixed analogue-digital integrated circuits, test costs

7 A new built-in self-test approach for digital-to-analog and analog-to-digital converters

Karim Arabi, Bozena Kaminska, Janusz Rzeszut

November 1994 **Proceedings of the 1994 IEEE/ACM international conference on Computer-aided design**

Publisher: IEEE Computer Society Press

Full text available: pdf(395.28 KB) Additional Information: full citation, abstract, references, citings, index terms

This paper proposes a test approach and circuitry suitable for built-in self-test (BIST) of digital-to-analog (D/A) and analog-to-digital (A/D) converters. Offset, gain, linearity and differential linearity errors are tested without using test equipment. The proposed BIST structure decreases the test cost and test time. The BIST circuitry has been designed to D/A and A/D converters using CMOS 1.2 &mgr;m technology. By only a minor modification the test structure would be able to localize th ...

8 A field-programmable mixed-analog-digital array

Paul Chow, P. Glenn Gulak

February 1995 **Proceedings of the 1995 ACM third international symposium on Field-programmable gate arrays**

Publisher: ACM Press

Full text available: pdf(113.78 KB) Additional Information: full citation, abstract, references, citings, index terms

A novel field-programmable mixed-analog-digital array (FPMA) is proposed, which contains a field-programmable analog array, a field-programmable digital array, and a mixed-signal interface. This device is intended to be used for the rapid implementation of mixed-signal circuits. The resource and architectural requirements for this array are determined by analyzing a set of sample circuits. The mixed-signal interface is constructed from converter blocks that contain configurable A/D and D/A ...

9 Efficient and accurate testing of analog-to-digital converters using oscillation-test method

K. Arabi, B. Kaminska

March 1997 **Proceedings of the 1997 European conference on Design and Test**

Publisher: IEEE Computer Society

Full text available: pdf(511.01 KB) Publisher Site Additional Information: full citation, abstract, citings

This paper describes a practical test approach for analog-to-digital converters (ADCs) based on the oscillation-test strategy. The oscillation-test is applied to convert the ADC under test to an oscillator. The oscillation frequencies are able to monitor the ADC conversion rate, differential nonlinearity (DNL) and integral nonlinearity (INL) at each quantization band edge (QBE). Using this method, no analog stimulus should be supplied and therefore the need for a costly precision signal generato ...

Keywords: A/D convertor, ADC conversion rate, ADC testing, analog-to-digital converters, analogue-digital conversion, differential nonlinearity, digital circuitry, integral nonlinearity, oscillation-test method, quantization band edge

10 System architectures for computer music

John W. Gordon

June 1985 **ACM Computing Surveys (CSUR)**, Volume 17 Issue 2

Publisher: ACM Press

Full text available: pdf(4.61 MB) Additional Information: full citation, abstract, references, citings, index terms, review

Computer music is a relatively new field. While a large proportion of the public is aware of computer music in one form or another, there seems to be a need for a better understanding of its capabilities and limitations in terms of synthesis, performance, and recording hardware. This article addresses that need by surveying and discussing the architecture of existing computer music systems. System requirements vary according to what the system will be used for. Common uses for co ...

11 An audio input-output computer system for medical information

Michael Otten, Scott I. Allen, Perry Plexico, William C. White
August 1969 **Proceedings of the 1969 24th national conference**
Publisher: ACM Press

Full text available: pdf(754.84 KB) Additional Information: full citation, abstract, references, citings, index terms

An experimental telephone-based input-output system, using low-cost audio response equipment, was implemented on a medium-sized real-time computer. This system enables update of audio vocabulary files from a remote telephone terminal, which is a major feature simplifying program and data base modification. Speech signals are processed with an analog-to-digital converter at the rate of 10,000 samples per second, compressed by a delta modulation program to one bit per sample, and stored on a ...

12 Macromodeling of analog circuits for hierarchical circuit design
Jianfeng Shao, Ramesh Harjani
November 1994 **Proceedings of the 1994 IEEE/ACM international conference on Computer-aided design**
Publisher: IEEE Computer Society Press

Full text available: pdf(933.09 KB) Additional Information: full citation, abstract, references, citings, index terms

Hierarchy plays a significant role in the design of digital and analog circuits. At each level of the hierarchy it becomes essential to evaluate if a sub-block design is feasible and if so which design style is the best candidate for the particular problem. This paper proposes a general methodology for evaluating the feasibility and the performance of sub-blocks at all levels of the hierarchy. A modified simplicial approximation technique is used to generate the feasibility macromodel and a ...

13 Ternary logic in a positional control system
H. T. Mouftah, K. C. Smith, Z. G. Vranesic
May 1976 **Proceedings of the sixth international symposium on Multiple-valued logic**
Publisher: IEEE Computer Society Press

Full text available: pdf(261.15 KB) Additional Information: full citation, abstract, references, index terms

The application of COS/MOS integrated circuits in the construction of a three-valued positional control system is presented. A unit-distance ternary code and the design of a ternary encoder are given. A ternary code converter able to translate this unit-distance code to the signed ternary code is described. A ternary threshold level detector is required to convert a noisy, slowly-changing, analog voltage into an abrupt digital logic change at required threshold levels. A three-valued compar ...

14 An interactive computer graphics approach to the design of marching band routines
M. G. Collins, G. R. Kane
July 1974 **Proceedings of the 1st annual conference on Computer graphics and interactive techniques**
Publisher: ACM Press

Full text available: pdf(11.27 KB) Additional Information: full citation, abstract

The availability of inexpensive minicomputers and low cost digital/ analog conversion equipment enables one to use interactive graphics in situations where such techniques were previously infeasible. The University of Tulsa is presently developing an interactive graphics package entitled BANDMARCH to assist the choreography of marching band drills. The graphics hardware consist of a 32K Interdata Model 70 minicomputer, a 5MB Diablo Disc system, a Tektronix 603 monitor with 256 x 256 point displa ...

15 A new algorithm for third generation circuit simulators: the one-step relaxation method
B. Hennion, P. Senn, D. Coquelle
June 1985 **Proceedings of the 22nd ACM/IEEE conference on Design automation**
Publisher: ACM Press

Full text available: pdf(620.43 KB) Additional Information: full citation, abstract, references, citings, index terms

A new algorithm for third generation circuit simulators, the "ONE-STEP RELAXATION METHOD" (OSR), as well as a concrete implementation of it, the new circuit simulator ELDO, are presented. This algorithm can replace the NEWTON method used in most second generation circuit simulators. Contrary to "Timing Simulator" algorithms, OSR makes no simplifying hypotheses on weak couplings between nodes. OSR performances are close to those of the "WAVEFORM RELAXATION METHO ...

16 A new algorithm for the design of stable higher order single loop sigma delta analog-to-digital converters

S. R. Kadivar, D. Schmitt-Landsiedel, H. Klar

December 1995 **Proceedings of the 1995 IEEE/ACM international conference on Computer-aided design**

Publisher: IEEE Computer Society

Full text available: pdf(341.91 KB) Additional Information: full citation, abstract, references, citings, index terms

Publisher Site

Abstract: This paper presents a new algorithm to attain optimized network scaling in single loop, 1 bit Sigma Delta Analog 1d Digital Converters (SD ADC) of order three or more. The algorithm is based on a novel mathematical description of stability and performance criteria of the SD ADC and on the application of nonlinear interactive optimization techniques. The feasibility of the new algorithm has been confirmed in practical implementations. The method brings new insight on the correlation bet ...

Keywords: CAD, SD ADC, analogue-digital conversion, convertors, electronic engineering computing, higher order, network scaling, nonlinear interactive optimization, performance criteria, sigma delta analog-to-digital converters, single loop

Results 1 - 16 of 16

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	24258	"713"/$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:42
L3	1	L2 and (((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined)adj6 (window or slot or period or time)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:15
L4	73	(((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined)adj6 (window or slot ot period or time)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:16
L5	1	L2 and (((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined or configurable or programmable)adj6 (window or slot or period or time)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:43
L6	16	(((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined or configurable or programmable)adj6 (window or slot or period or time)))same (clock or clk or (chip adj select))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 14:31

EAST Search History

L7	38836	"341"/$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:42
L8	17	L7 and (((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined or configurable or programmable)adj6 (window or slot or period or time)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 14:28
L9	5	l7 and ((((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined or configurable or programmable)adj6 (window or slot or period or time)))same (clock or clk or (chip adj select)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:45
L10	0	l2 and ((((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined or configurable or programmable)adj6 (window or slot or period or time)))same (clock or clk or (chip adj select)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:44
L11	4024	(assert$4 or dassert$4 or inactivat$4 or activat$4 or enabl$4 or disabl$4)near3 ((device or chip)adj select)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 14:41
L12	20	l11 with(((user or predefined or predetermined or configurable or programmable)adj6 (window or slot or period or time)))same (clock or clk or (chip adj select))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 14:35

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L13	5	l12 same ((operati$4 or function$4 or power or energy) near3 (mode or state))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 14:35
L14	49	l11 same(((user or predefined or predetermined or configurable or programmable)adj6 (window or slot or period or time)))same (clock or clk or (chip adj select))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 14:43
L15	10	l14 same ((operati$4 or function$4 or power or energy) near3 (mode or state))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 14:44
L16	286	((toggl$4 or switch$4 or chnag$4 or alter$5)near3 (state or mode or logic))with((device or chip)adj select)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 14:43
L17	13	l16 same(((user or predefined or predetermined or configurable or programmable)adj6 (window or slot or period or time)))same (clock or clk or (chip adj select))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 14:50
L18	4	l17 same ((operati$4 or function$4 or power or energy) near3 (mode or state))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 14:44
L19	3	((toggl$4 or switch$4 or chnag$4 or alter$5)near5((((power$3 or energy)near2 (partial$3 or full$3 or up))or (daisy adj chain))adj (state or mode)))with ((user or operator or predefined or predetermined)near3 (time or window or period or slot))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 14:57

EAST Search History

L20	3	((toggl$4 or switch$4 or chnag$4 or alter$5)near5((((power$3 or energy)near2 (partial$3 or full$3 or up))or (daisy adj chain))adj (state or mode)))with ((user or operator or predefined or predetermined)near3 (time or window or period or slot or duration))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 14:58
L21	3	((toggl$4 or switch$4 or chnag$4 or alter$5)near5((((power$3 or energy)near2 (partial$3 or full$3 or up))or (daisy adj chain))adj (state or mode)))with ((user or operator or predefined or predetermined or number)near3 (clock41 or puls$2 or cycle$2 or time or window or period or slot or duration))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 14:59

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L2	24258	"713"/$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:42
L3	1	L2 and (((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined)adj6 (window or slot or period or time)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:15
L4	73	(((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined)adj6 (window or slot ot period or time)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:16
L5	1	L2 and (((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined or configurable or programmable)adj6 (window or slot or period or time)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:43
L6	16	(((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined or configurable or programmable)adj6 (window or slot or period or time)))same (clock or clk or (chip adj select))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:44

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L7	38836	"341"/$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:42
L8	17	L7 and (((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined or configurable or programmable)adj6 (window or slot or period or time)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:48
L9	5	l7 and ((((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined or configurable or programmable)adj6 (window or slot or period or time)))same (clock or clk or (chip adj select)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:45
L10	0	l2 and ((((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined or configurable or programmable)adj6 (window or slot or period or time)))same (clock or clk or (chip adj select)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:44
S1	4	("5619204" "5714955").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 08:12
S2	5	("4045719" | "5287525" | "5420798" | "5543795" | "5557274").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/22 08:23
S3	5	("5714955").URPN.	USPAT	OR	ON	2006/02/22 08:26

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S4	10	("5294928" | "5422807" | "5619204" | "5714955" | "5886658" | "5914681" | "6057795" | "6070140" | "6072417" | "6163851").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/22 08:28
S5	5	("5714955").URPN.	USPAT	OR	ON	2006/02/22 08:33
S6	5	("6057795").URPN.	USPAT	OR	ON	2006/02/22 08:34
S7	4	("3610958" | "5294928" | "5619204" | "5754135").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/22 08:37
S8	2460	(analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4))	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/22 08:43
S9	2	S8 same (((respons$4 or result$4)adj4 (number near3 (clock$1 or puls$2)))with ((control or status or select)adj (signal or input)))	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/22 11:11
S10	2975	(analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 08:46
S11	2	S10 same (((respons$4 or result$4)adj4 (number near3 (clock$1 or puls$2)))with ((control or status or select)adj (signal or input)))	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/22 08:45
S12	2	S10 same (((respons$4 or result$4)adj4 (number near3 (clock$1 or puls$2)))same ((control or status or select)adj (signal or input)))	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/22 08:45
S13	2	S10 same (((respons$4 or result$4)adj4 (number near3 (clock$1 or puls$2)))with ((control or status or select)adj (signal or input)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 08:46
S14	24258	"713"/$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 08:46

EAST Search History

S15	38836	"341"/$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 08:46
S16	1	S14 and (((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same(((respons$4 or result$4)adj4 (number near3 (clock$1 or puls$2)))with ((control or status or select)adj (signal or input))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 08:50
S17	1	S15 and (((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same(((respons$4 or result$4)adj4 (number near3 (clock$1 or puls$2)))with ((control or status or select)adj (signal or input))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 08:48
S18	1	S14 and (((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined)near3 (window or slot ot period or time)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 08:51
S19	17	S15 and (((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined)near3 (window or slot ot period or time)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 11:22

EAST Search History

S20	17	S15 and (((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined)near3 (window or slot or period or time)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 11:11
S21	1	S14 and (((analogtodigital or (analog adj4 converter))with ((mode or state)near3 (operati$4 or function$4 or selct$4 or control$4 or switch$4 or alter$5 or chang$4)))same((user or predefined or predetermined)near3 (window or slot ot period or time)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/22 12:14